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APPLICATION NO.	FIL	ING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/839,680	04/20/2001		Robin Speed	MS1-600US	1810
22801	7590	03/24/2006		EXAMINER	
LEE & HA			CZEKAJ,	CZEKAJ, DAVID J	
421 W RIVERSIDE AVENUE SUITE 500 SPOKANE, WA 99201				ART UNIT	PAPER NUMBER
,				2621	

DATE MAILED: 03/24/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)	
	09/839,680	SPEED ET AL.	
Office Action Summary	Examiner	Art Unit	
	Dave Czekaj	2616	
The MAILING DATE of this communication app Period for Reply	*	orrespondence address	
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 16(a). In no event, however, may a reply be time 11 apply and will expire SIX (6) MONTHS from the cause the application to become ABANDONE	N. nely filed the mailing date of this communication. D (35 U.S.C. § 133).	
Status			
1) ☐ Responsive to communication(s) filed on <u>26 Oct</u> 2a) ☐ This action is FINAL . 2b) ☐ This 3) ☐ Since this application is in condition for allowar closed in accordance with the practice under E	action is non-final. nce except for formal matters, pro		
Disposition of Claims			
4) ☐ Claim(s) 1-20 is/are pending in the application. 4a) Of the above claim(s) is/are withdray 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 1-20 is/are rejected. 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restriction and/or	vn from consideration. r election requirement.		
9) The specification is objected to by the Examine 10) The drawing(s) filed on is/are: a) access applicant may not request that any objection to the objection to the object of the control of the control of the object of the control of the object of the object of the object of the control of the object	epted or b) objected to by the l drawing(s) be held in abeyance. Sec ion is required if the drawing(s) is ob	e 37 CFR 1.85(a). jected to. See 37 CFR 1.121(d).	
Priority under 35 U.S.C. § 119			
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the priority application from the International Bureau * See the attached detailed Office action for a list	s have been received. s have been received in Applicati ity documents have been receive ı (PCT Rule 17.2(a)).	on No ed in this National Stage	
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 9/16/05.	4) Interview Summary Paper No(s)/Mail Di 5) Notice of Informal F 6) Other:		

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DETAILED ACTION

Response to Arguments

On page 11, applicant argues that the examiners motivation is to general and does not address why the specific proposed modification would have been obvious. While the applicant's points are understood, the examiner respectfully disagrees. The examiner notes that Rose discloses in column 2, lines 10-14, a problem that prior art coding systems have is being able to take advantage of additional information without causing undesirable conflicts in the system. Therefore, the Radha reference, combined with the teachings of Rose, would provide a system that operates more efficiently within the context of being able to use the additional information without causing undesired conflicts. Further, it appears applicant is attacking the references used in the prior art rejection individually and is alleging that the examiner uses hindsight construction to come up with applicant's claimed invention. However, such attempt has been considered by the court improper. The prior art rejection under 35 USC 103 is based on a combination of references. One cannot show non-obviousness by attacking references individually. In re Keller, 208 USPQ 871 (CCPA 1981). Furthermore, In re McLaughlin, 170 USPQ 209 (CCPA 1971) states that "[T]he test for combining references is not what the individual references themselves suggest but rather what the combination of the disclosures taken as a whole would suggest to one of ordinary skill in the art. Any judgement on obviousness is in a sense necessarily a reconstruction based upon hindsight reasoning, but so long as it takes into account only knowledge which was within the level of ordinary skill at the time the claimed invention was made

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and does not include knowledge gleaned from applicant's disclosure, such a reconstruction is improper[.]". Therefore the rejection has been maintained.

On page 11, applicant agues that the office fails to establish a prima facie case of obviousness. While the applicant's points are understood, the examiner respectfully disagrees. As previously stated, Rose discloses in column 2, lines 10-14, a problem that prior art coding systems have is being able to take advantage of additional information without causing undesirable conflicts in the system. Therefore, the Radha reference, combined with the teachings of Rose, would provide a system that operates more efficiently within the context of being able to use the additional information without causing undesired conflicts. Rose further discloses in column 2 line 67 – column 3, line 2, another problem of the standard approach is that no advantage is taken of the potentially superior prediction information. Therefore, the Radha reference, combined with the teachings of Rose, would provide a system that operates more efficiently within the context of being able to take advantage of the superior prediction information. Therefore the rejection has been maintained.

Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claims 1-20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Radha et al. (6639943), (hereinafter referred to as "Radha") in view of Rose (6731811).

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Regarding claims 1, 8, 9, 10, and 19, Radha discloses an apparatus that relates to fine granular coding that includes both quality and temporal scalability (Radha: column 1, lines 11-13). This apparatus comprises "generating a motion compensated prediction of a region of content" (Radha: figure 7, column 7, lines 49-51, wherein the motion compensated prediction is generated by the motion estimation block, the region of content is the frames/streams), "receiving an indication of whether there are first and second quantities of residual samples remaining" (Radha: figures 5A and 8A, wherein the residual samples are contained with the enhancement and base layers, the indication is the process from moving from one layer to the next. The examiner notes that the apparatus would not move from one layer to the next without all necessary data needed for further processing. Therefore a move from one layer to the next would indicate whether there are first and second quantities of residual samples) and "adding the first quantity of residual samples to the prediction" (Radha: figure 7, column 3. lines 15-27). However, Radha fails to show the subtraction as claimed. Rose teaches that prior art coding systems cause undesired conflicts when trying to take advantage of additional information available to the enhancement layer (Rose: column 2, lines 10-14). To help alleviate this problem, Rose discloses "subtracting the second quantity of residual samples from the refined prediction value to generate a final representation" (Rose: figure 5, wherein the second set of residual samples is the multiple enhancement layers). Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention

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was made to take the apparatus disclosed by Radha and add the subtraction method taught by Rose in order to obtain an apparatus that operates more efficiently by being able to take advantage of additional information given to a system without causing undesired conflicts.

Regarding claims 2 and 13, Radha discloses "the first and second residual samples are eight bit samples" (Radha: column 5, lines 37-45, wherein the pixel represents one byte or eight bits).

Regarding claims 3, 14, and 17, Radha discloses "performing an inverse discrete cosine transform of decoded transform domain representation of residual differences to be added to the motion compensated prediction" (Radha: figure 10, wherein the inverse discrete cosine transform is the inverse DCT, the addition is performed by the adder (item 58), and the motion compensated prediction is performed by the motion compensation block).

Regarding claims 4, 7, 12, and 18, although not disclosed, the region of content could comprise a macroblock (Official Notice). Doing so would have been obvious since macroblocks are well known in the MPEG environment.

Regarding claims 5 and 16, Radha discloses "generating a prediction of media is performed by a graphics accelerator under the control of a decoder application" (Radha: figure 10, wherein the accelerator comprises the motion compensation and inverse DCT blocks).

Regarding claim 6, Radha discloses "sending prediction control information necessary for generation of a motion compensated prediction to the

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accelerator" (Radha: figure 10, wherein the accelerator comprises the motion compensation and inverse DCT blocks, the prediction control information is the enhancement and base layers and inverse quantization parameters), "sending an indication and the samples to the accelerator of whether the first and second samples are to be applied" (Radha: figures 5A and 8A, wherein the indication is the process from moving from one layer to the next indicating there are layers remaining), and "performing processing at the accelerator (Radha: figure 10, wherein the accelerator comprises the motion compensation and inverse DCT blocks which perform processing of the samples).

Regarding claim 11, note the examiners rejection for claims 5 and 6.

Regarding claims 15 and 20, Radha discloses the complementary decoder performing the operations of the encoder disclosed in the preceding claims. The decoder also comprises an "application program interface" (Radha: column 9, lines 57-59, wherein the interface is the application that runs to obtain the user input).

Conclusion

3. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the

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shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dave Czekaj whose telephone number is (571) 272-7327. The examiner can normally be reached on Monday - Friday 9 hours.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mehrdad Dastouri can be reached on (571) 272-7418. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

DJC

PRIMARY EXAMINER